



S/N 09/945512

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|-------------|--|-----------------|---------------|
| Applicant: | Leonard Forbes | Examiner: | Richard Booth |
| Serial No.: | 09/945512 | Group Art Unit: | 2812 |
| Filed: | August 30, 2001 | Docket: | 1303.027US1 |
| Title: | IN SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS | | |

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

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Dkt: 1303.027US1

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

LEONARD FORBES

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.

P.O. Box 2938

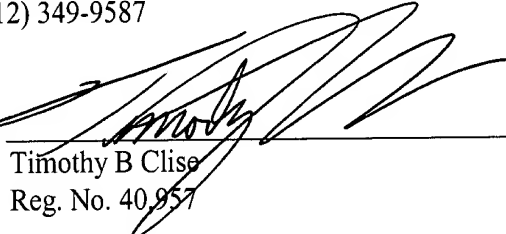
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Date

17 Sept '03

By


Timothy B Clise

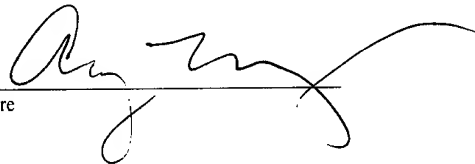
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 17th day of September, 2003.

Name

Amy Moriarty

Signature



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| Substitute for form 1449A/PTO | | Complete if Known | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary) | | Application Number | 09/945512 |
| | | Filing Date | August 30, 2001 |
| | | First Named Inventor | Forbes, Leonard |
| | | Group Art Unit | 2812 |
| | | Examiner Name | Booth, Richard |
| Sheet 1 of 1 | | Attorney Docket No: 1303.027US1 | |



| US PATENT DOCUMENTS | | | | | | |
|---------------------|---------------------|------------------|---|-------|----------|----------------------------|
| Examiner Initial * | USP Document Number | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | Filing Date If Appropriate |
| | US-5,952,692 | 09/14/1999 | Nakazato, K. , et al. | 257 | 321 | 10/28/1997 |
| | US-6,210,999 | 04/03/2001 | Gardner, , et al. | 438 | 183 | 12/04/1998 |
| | US-6,475,857 | 11/05/2002 | Kim, W. , et al. | 438 | 240 | 06/21/2001 |
| | US-6,541,280 | 04/01/2003 | Kaushik, , et al. | | | 03/20/2001 |

| FOREIGN PATENT DOCUMENTS | | | | | | |
|--------------------------|---------------------|------------------|---|-------|----------|----------------|
| Examiner Initials* | Foreign Document No | Publication Date | Name of Patentee or Applicant of cited Document | Class | Subclass | T ² |

| OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS | | | |
|--|----------------------|---|----------------|
| Examiner Initials* | Cite No ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | SHI, Y. , "Tunneling Leakage Current in Ultrathin (<4 nm) Nitride/Oxide Stack Dielectrics", IEEE Electron Device Letters, 19(10), (1998),pp. 388-390 | |
| | | ZHANG, "Atomic Layer Deposition of High Dielectric Constant Nanolaminates", Journal of The Electrochemical Society, 148(4),(2001),F63-F66 | |

EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 909. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached